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	Application No.	Applicant(s)
Notice of Allowability	10/017,933	CHANG ET AL.
	Examiner	Art Unit
	Kenneth B. Wells	2816
The MAILING DATE of this communication appears on the cover sheet with the correspondence address All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS. This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308. 1. This communication is responsive to telephone interview on 11/23/04.		
2. ⊠ The allowed claim(s) is/are <u>1,3-10,12-15 and 17-28</u> .		
3. ⊠ The drawings filed on <u>14 December 2001</u> are accepted by the Examiner.		
4.		
Attachment(s) 1. ☐ Notice of References Cited (PTO-892) 2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-948) 3. ☐ Information Disclosure Statements (PTO-1449 or PTO/SB/0 Paper No./Mail Date	6. ☑ Interview Summa Paper No./Mail I 8), 7. ☑ Examiner's Amei	al Patent Application (PTO-152) ary (PTO-413), Date 11 23 04 Indment/Comment Indexended the second seco

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EXAMINER'S AMENDMENT

An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with Frank Morris on 11/23/04.

IN THE CLAIMS

In claim 25, line 6, after "circuit", the following has been inserted: --, wherein only one source follower circuit is selected at any one time--.

REASONS FOR ALLOWANCE

The following is an examiner's statement of reasons for allowance:

Each of the independent claims is now allowable over Shin in view of the limitation in each that only one of the circuits which provides a path between node 140 and ground (or node 150 and Vcc) is selected at any one time. This is because in Shin's Fig. 3, the four parallel paths between circuit 30 and ground (or between circuit 30 and VDD) can be active at the same time,

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i.e., if any two or more of the gate voltages of FETs MP3 through MP6 is low logic at the same time).

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Kenneth B. Wells whose telephone number is (571)272-1757. The examiner can normally be reached on Monday through Friday from 8:30am to 5:00pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Timothy P. Callahan, can be reached at (571)272-1740. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through

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November 23, 2004